



# CANARA ENGINEERING COLLEGE

BENJANAPADAVU – 574219, Bantwal Taluk, D K District, Karnataka

[www.canaraengineering.in](http://www.canaraengineering.in)

## Feed back on Faculty - Department of ECE Sample Feedback forms - Pg. No. 2 Feedback Analysis - Pg. No. 5

**Canara Engineering College**  
**Benjanapadavu, Bantwal Talu Mangaluru**

**Electronics & Communication Engineering**  
**Admission Year 2015-2016**  
**HDL Lab (15ECL58)**  
**Mohan A R**

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**Course Feedback Report**

**Total students: 101**

**Percentage=96.63%**

Sl. No.	Feedback Parameters	Scores
1	Is regular and punctual in taking up of classes	4.88
2	Demonstrates sound knowledge on the subject	4.82
3	Provides additional knowledge on the subject beyond syllabus	4.81
4	Is fair in dealing with students	4.80
5	Creates an atmosphere conducive to learning	4.83
6	Deals effectively with student's behaviour	4.84
7	Provides notes that is a good match for the objectives	4.82
8	Plans activities that are well differentiated and appreciated by students	4.84
9	Welcomes/settles the class appropriately and displays humble but commanding skills	4.82
10	Makes objectives if the session/topic explicit to students	4.81
11	Engages the class with activities that are appropriate and creative	4.84
12	Encourage them to think	4.85

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**CO Feedback**

**Percentage=4.78%**

**Canara Engineering College**  
**Benjanapadavu, Bantwal Talu Mangaluru**

**Electronics & Communication Engineering**  
**Admission Year 2016-2017**  
**Control Systems (15EC43)**  
**Anirudh M Kamath**

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**Course Feedback Report**

**Total students: 42**

**Percentage=89.40%**

Sl. No.	Feedback Parameters	Scores
1	Is regular and punctual in taking up of classes	4.55
2	Demonstrates sound knowledge on the subject	4.57
3	Provides additional knowledge on the subject beyond syllabus	4.40
4	Is fair in dealing with students	4.38
5	Creates an atmosphere conducive to learning	4.48
6	Deals effectively with student's behaviour	4.45
7	Provides notes that is a good match for the objectives	4.48
8	Plans activities that are well differentiated and appreciated by students	4.48
9	Welcomes/settles the class appropriately and displays humble but commanding skills	4.50
10	Makes objectives if the session/topic explicit to students	4.40
11	Engages the class with activities that are appropriate and creative	4.48
12	Encourage them to think	4.48

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**CO Feedback**

**Percentage=4.03%**

**Canara Engineering College**  
**Benjanapadavu, Bantwal Talu Mangaluru**

**Electronics & Communication Engineering**  
**Admission Year 2015-2016**  
**Embedded Controller Lab(15ECL67)**  
**Savitha . Acharya**

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**Course Feedback Report**

**Total students: 114**

**Percentage=80.54%**

Sl. No.	Feedback Parameters	Scores
1	Is regular and punctual in taking up of classes	4.43
2	Demonstrates sound knowledge on the subject	4.08
3	Provides additional knowledge on the subject beyond syllabus	3.92
4	Is fair in dealing with students	3.96
5	Creates an atmosphere conducive to learning	3.99
6	Deals effectively with student's behaviour	4.02
7	Provides notes that is a good match for the objectives	3.83
8	Plans activities that are well differentiated and appreciated by students	3.90
9	Welcomes/settles the class appropriately and displays humble but commanding skills	4.05
10	Makes objectives if the session/topic explicit to students	3.97
11	Engages the class with activities that are appropriate and creative	3.91
12	Encourage them to think	4.26

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**CO Feedback**

**Percentage=3.80%**



# CANARA ENGINEERING COLLEGE

Benjanapadavu, Bantwal Taluk  
Mangalore – 574219



DEPARTMENT OF ELECTRONICS AND COMMUNICATION

## ANALYSIS OF STUDENTS FEEDBACK ON FACULTY

Academic Year: 2017-2018

Sem: Odd

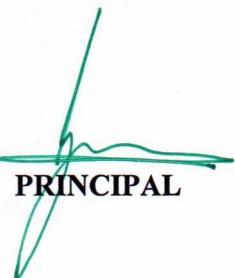
Sl. No.	Faculty name	Course name	Course code	Feedback
1.	Vayusutha M	Digital Signal Processing	15EC52	96.46%
2.	Mohan A R	Verilog HDL	15EC53	94.09%
3.	Ajith Kumar B P	Verilog HDL	15EC53	94.33%
4.	Akshatha Ganesh Baliga	Information Theory & Coding	15EC54	84.81%
5.	Dr. Ganesh V Bhat	Object Oriented Programming U sing C++	15EC562	74.31%
6.	Vayusutha M	DSP Lab	15ECL57	91.27%
7.	Mohan A R	HDL Lab	15ECL58	96.63%

### Observation:

The average faculty feedback obtained for this academic year is 90.27% with lowest=74.31% and highest=96.63%.

### Action suggested:

As per College Policy, it is expected that every faculty get minimum feedback of 70% in their respective course. Since all faculties have obtained feedback is above 70% it is suggested to the concerned faculty to continue the same good work and improve further.

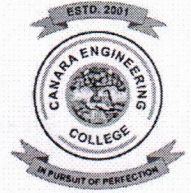
  
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# CANARA ENGINEERING COLLEGE

Benjanapadavu, Bantwal Taluk  
Mangalore – 574219



DEPARTMENT OF ELECTRONICS AND COMMUNICATION

## ANALYSIS OF STUDENTS FEEDBACK ON FACULTY

Academic Year: 2017-2018

Sem: Even

Sl. No.	Faculty name	Course name	Course code	Feedback
1.	Chaithra	Engineering Mathematics - IV	15MAT41	80.32%
2.	Reena Pednekar	Engineering Mathematics - IV	15MAT41	87.47%
3.	Raghavendra Sooda	Microprocessor	15EC42	80.07%
4.	Raghavendra M Shetty K	Control Systems	15EC43	74.42%
5.	Anirudh M Kamath	Control Systems	15EC43	89.40%
6.	Vayusutha M	Signals and Systems	15EC44	96.42%
7.	Sreerama Samartha	Principles of Communication Systems	15EC45	91.16%
8.	Vinay H S	Principles of Communication Systems	15EC45	80.83%
9.	Padmahasa Mattadamooole	Linear Integrated Circuits	15EC46	90.53%
10.	Anoop Kumar PKG	Linear Integrated Circuits	15EC46	65.00%
11.	Mohan A R	Microprocessor Lab	15ECL47	89.96%
12.	Sreerama Samartha	Linear ICs and Communication Lab	15ECL48	84.90%
13.	Vinay H S	Linear ICs and Communication Lab	15ECL48	86.83%
14.	Kavya A D	Digital Communication	15EC61	61.60%
15.	Ajith Kumar B P	ARM Microcontroller and Embedded System	15EC62	93.60%
16.	Dr. Ganesh V Bhat	ARM Microcontroller and Embedded System	15EC62	68.03%
17.	Savitha Acharya	VLSI Design	15EC63	70.98%
18.	Mohan A R	VLSI Design	15EC63	89.02%
19.	Jayaprasad K M	Computer Communication networks	15EC64	80.17%
20.	Naidile S	DSS	15EC654	79.55%
21.	Dr. Dayananda G K	Digital System Design using Verilog	15EC663	56.02%
22.	Savitha Acharya	Embedded Controller Lab	15ECL67	80.54%



23.	Jayashree K	Computer networks Lab	15ECL68	76.65%
24.	Ajeya B	Wireless Communication	10EC81	84.46%
25.	Sandeep Prabhu M	Digital Switching System	10EC82	85.29%
26.	Akshatha Ganesh Baliga	Network Security	10EC832	84.19%
27.	Jayashree K	Network Security	10EC832	88.62%
28.	Rajitha A A	GSM	10EC843	86.68%
29.	Anirudh M Kamath	Seminar	10ECS86	89.54%

**Observation:**

The average faculty feedback obtained for this academic year is 81.80% with lowest=56.02% and highest=96.42%.

**Action suggested:**

As per College Policy, it is expected that every faculty get minimum feedback of 70% in their respective course. But in the current semester, it is observed that the following faculty have feedback less than 70%

Sl. No.	Faculty name	Course name	Course code	Action Suggested
1	Anoop Kumar PKG	Linear Integrated Circuits	15EC46	It is suggested that in the next semester the performance needs to be improved by adopting effective teaching learning methodology.
2	Kavya A D	Digital Communication	15EC61	Requested to change subject in next semester as the subject was not matching with her expertise.
3	Dr. Ganesh V Bhat	ARM Microcontroller and Embedded System	15EC62	Requested to change subject in next semester as the subject was not matching with his expertise.
4	Dr. Dayananda G K	Digital System Design using Verilog	15EC663	It is suggested that in the next semester the performance needs to be improved by adopting effective teaching learning methodology.

  
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